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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: <b>Harer et al.</b>	Art Unit: <b>2123</b>
Application No. <b>09/760,063</b>	Examiner: <b>Thomas H. Stevens</b>
Filed: <b>01/12/01</b>	Confirmation No. <b>2596</b>
For: <b>SIMULATION-BASED FUNCTIONAL VERIFICATION OF MICROCIRCUIT DESIGNS</b>	Attorney Docket: <b>06816.0173 (was 4000/9)</b>

**Response**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed Jun. 30, 2005, applicants submit the present Response on Dec. 19, 2005.